

Information

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Educational Background

Sep. 2018 - Jun 2022

School of Microelectronics, Department of Integrated Circuits, Xidian University
Integrated Circuit Design and Integrated System (Bachelor)

Research Topics: P-GaN gate power devices (HEMT)

Undergraduate Supervisor: Mao Wei, Doctor of Microelectronics and Solid-State Electronics at Xidian University, Professor in the Department of Integrated Circuits at Xidian University, has long been engaged in research on new semiconductor devices and integrated circuits, power semiconductor devices and circuits.

Sep. 2024 - Jun 2026

Department of Materials Science and Engineering, City University of Hong Kong (Dongguan)
Materials Engineering and Nanotechnology (Master)

Research Topics:

- (1) Thin-Film Lithium Niobate Homogeneous Integrated Electro-Optic/Acousto-Optic Modulator
- (2) Surface Acoustic Wave (SAW) Band-Pass Filter
- (3) Ga₂O₃ Optoelectronic Memristors and Artificial Neural Networks (ANN)

Graduate Supervisor: Sun Wenzhao, Ph.D. in Optoelectronics from Harbin Institute of Technology (Shenzhen), Postdoctoral Fellow in the Department of Electrical Engineering at City University of Hong Kong, Professor at City University of Hong Kong (Dongguan). His primary research focuses on on-chip micro-nano photonic integration technologies for silicon-based and thin-film lithium niobate materials. Su Longxing, Ph.D. in Condensed Matter Physics from Sun Yat-sen University, Researcher at the International School of Microelectronics, Dongguan University of Technology. His main research focuses on the epitaxial growth and controllable synthesis of semiconductor/dielectric materials, optoelectronic devices, ferroelectric/resistive switching devices, and device physics.

Research Experience

Jun 2022 - Sep. 2024

Southern University of Science and Technology
Research Assistant

Research Topics:

- (1) 1S1R-Type RRAM Array Based on Mott Memristors
- (2) Silicon-Based PIN Photodetector
- (3) NAND Flash Memory Cell Based on Ferroelectric Thin-Film Transistors

Supervisor: Li Yida, Ph.D. in Electrical and Computer Engineering from the National University of Singapore (NUS), Senior Staff Engineer at TSMC, Professor at the Southern University of Science and Technology (SUSTech). Primary research interests include post-Moore memory/logic/optoelectronic devices, 3D monolithic integration processes, and compute-in-memory system architectures.

Research Achievements: 2 EI papers(IEEE ICICDT) and 1 SCI paper(IEEE Electron Device Letters, under reviewed)

Awards and Achievements

In 2019, won the Provincial Third Prize in the National College Students Mathematics Competition.
In 2021, won the Provincial Third Prize in the National College Students Mathematics Competition.
In 2022, won the ChinaResources Scholarship from the School of Microelectronics of Xidian University